

Testbed Name	Milestone(s) Supported	Vendor	Model	"Sustained" Performance	Number of Processors	Processor Type	Total Memory	Total Memory Bandwidth	Attached Disk Space	Location	Date Acquired (Actual or Planned)	Date Retired (Actual or Planned)	How Retired	Total Acquisition Cost to Government	Total Acquisition Cost to NASA HPCC Program	Acquisition Method	Annual Maintenance Cost (Hardware) to NASA HPCC Program	Source of Funds	Resource Allocation	Special Notes
				(Gflops)			(Mb)	(Mb/s)						(\$ Millions)	(\$ Millions)		(\$K-Thousand)			
				(Linpack)																
Lomax	CAS (HPCCP 1.1, 2.2, 2.3, 5.3, 6.3)/ESS	SGI	Origin	196	512	R12000 300 Mhz	196,000	400 (to main memory per processor)	154.7 GB +1.48 TB RAID	Ames	Jun-99	TBD	TBD	17.05	9.30	SEWP II	\$0.00 first year \$1.2 M per year after 1 year	HPCC/CAS-\$2.250M HPCC/ESS-\$7.05M ITBase-\$2M Code S-\$0.25M Other-\$5.5M	HPCC/CAS-22.5% HPCC/ESS-50% ITBase-22.5% SGI-5%	CoSMO funds in return for transfer of Steger to CoSMO
Steger	CAS (HPCCP 1.1, 2.2, 2.3, 5.3, 6.3)/ESS	SGI	Origin	101	256	R10000 250 Mhz	64,000	400 (to main memory per processor)	111.8 GB + 800 GB RAID	Ames	Feb-98	May-00	Turned over to CoSMO	9.70	4.85	SEWP II	\$0.00 first year \$555K per year after 1 year	HPCC/CAS-\$4.85M ITBase-\$4.85M	CoSMO ---100%	Prior to May 1, 2000: HPCCP CAS had 32.5%, IT base had 32.5%, CoSMO had 30%, SGI had 5%.
Hopper	CAS (HPCCP 1.1, 2.2, 2.3, 5.3, 6.3)/ESS	SGI	Origin	25	64	R10000 250 Mhz	1,600	400 (to main memory per processor)	36 GB + 540 GB RAID	Ames	Dec-97	TBD	TBD	2.50	1.25	SEWP II	\$0.00 first year \$260K per year after 1 year	HPCC/CAS-\$1.25M ITBase-\$1.25M	HPCC/CAS-25% HPCC/ESS-25% ITBase-25% CoSMO-25%	Prior to May 1, 2000: HPCCP CAS had 35%, IT base had 35%, CoSMO had 30%
Whitcomb	CAS (HPCCP 1.1, 2.2, 2.3, 5.3, 6.3)/ESS	SGI	Origin	7	16	R10000 250 Mhz	12,288	400 (to main memory per processor)	27 GB + 132 GB RAID	Langley	Jun-98	TBD	TBD	0.36	0.36	SEWP II	\$0.00 first year \$60K per year after 1 year	HPCC/CAS-\$0.355M	HPCC/CAS-100%	
Sharp	CAS (HPCCP 1.1, 2.2, 2.3, 5.3, 6.3)/ESS	SGI	Origin	10	24	R1000 250 Mhz (16) R1000 195 MHz (8)	4,000	400 (to main memory per processor)	128 GB	Glenn	Jun-98	Sep-01	TBD	0.36	0.36	SEWP II	\$0.00 first year \$60K per year after 1 year	HPCCP/CAS-\$0.355M	HPCC/CAS-100%	
Jsimpson	ESS (HPCC 1.1, 1.3)	Cray	T3E	607	1360	Alpha EV5.6 300 Mhz	162,000	1200 (to memory per processor)	2.2TB	GSFC	Apr 98 - Jan 00	CoSMO decision	Turn over to CoSMO	20.50	13.10	SEWP II	\$1.0M per year	NCCS-\$7.4M HPCC/ESS-\$13.1M	NSIPP-80% HPCC/ESS-20%	ESS jobs run on total system by arrangement, FY96-99 ESS had 100% access to 512 processors
theHive	ESS (HPCC 2.2, 7.1)	various PC vendors	Linux cluster	15	200	Intel P6 200-500 Mhz	42,000	180 (to memory per processor)	1.0TB	GSFC	Sep 97 - Apr 99	FY02 - FY03	TBD	0.50	0.50	Small Purchase	\$3K per year	HPCC/ESS-\$0.5M	First-come First-served	
REE Level Zero Testbed	REE (HPCC 1.2, 5.1)	CETIA	na - Assembled at JPL	7	6 Nodes/ 12 Processors	PPC 750	768 (128 per node)	Std PCI	53 GB	JPL	Jan 00	TBD	TBD	0.14	0.14	Fixed Price	No formal maintenance agreement	HPCC/REE-\$0.14M	HPCC/REE-100%	Sustained performance is for a 6 Node system
REE First Generation Testbed	REE (HPCC 1.2, 2.1, 5.1, 5.4, 6.2, 7.2)	Sanders, a Lockheed Martin Co.	n/a - Built to Spec	24	20 Nodes/ 40 Processors	PPC 750	2560 (128 per node)	Std PCI	161 GB	Sanders home facility, Nashua, NH	June 00 (Planned)	Mar 04 (Planned)	TBD	4.94	4.94	Fixed Price	\$0.106/yr for first two years	HPCC/REE-\$4.94M	HPCC/REE-100%	Sustained performance is for a 20 Node system
TOTALS				992										56.04	34.79					